# Testing

* Decoding:
  + Applied HW2 and previous knowledge from CSE31.
  + Bitwise operations to get the correct bits into the D struct.
  + R format straight-forward, I format check if negative integer. J format simple.
  + At the end, D struct holds the correct values into the registers.
* Executing:
  + Switch cases for different formats and operations.
  + Based on function/op, execute the appropriate math.
  + After, return that value to be sent to the next stage.
* Memory:
* RegWrite:
  + Depending on the Format, changedReg holds the correct register to change.
  + R format, rd
  + I Format, rt
  + J format, none.
  + If chnagedReg is -1, no changed registers will be printed.